



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/751,353	12/27/2000	Jainendra Kumar	CISCO-3479	8818

7590 08/09/2005
Kenneth D'Alessandro
Sierra Patent Group, Ltd.
P.O. Box 6149
Stateline, NV 89449

EXAMINER

LIPMAN, JACOB

ART UNIT	PAPER NUMBER
----------	--------------

2134

DATE MAILED: 08/09/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

09/751,353

Applicant(s)

KUMAR, JAINENDRA

Examiner

Jacob Lipman

Art Unit

2134

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 23 May 2005.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-28 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-28 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 27 December 2000 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☐ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: _____

DETAILED ACTION

Claim Rejections - 35 USC § 103

1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

2. Claims 1-28 are rejected under 35 U.S.C. 103(a) as being unpatentable over Le et al., US Patent number 5,883,956.

With regard to claims 1, 17, 21, and 25, Le discloses a computer with a cryptographic chip having several cryptographic algorithms (column 7 lines 37-40), and encrypted token (column 5 lines 33-52) in non-volatile memory (column 6 lines 51-53) which when decrypted with system-specific information (column 6 lines 1-7, column 11 line 57-column 12 line 5), enables a desired set of cryptographic capabilities (column 5 lines 53-55). Le does not disclose the initialization information should be encrypted in the non-volatile memory. Le does disclose that encrypting initialization information increases security (column 7 lines 5-15) and increase tamper resistance (column 5 lines 12-20). While Le does not see it necessary to encrypt the information in the ROM, it would be obvious to one of ordinary skill in the art to encrypt the initialization information to make the ROM more tamper-proof. Le discloses motivation to make the ROM tamper-proof (column 6 lines 43-54).

With regard to claim 3, 20, 24, and 28, Le discloses that the system-specific information can be the MAC address (column 6 lines 1-4).

With regard to claim 4, 19, 23, and 27, Le discloses hashing the public key with the device ID (column 11 lines 59-64), thus creating a private key. Le also discloses the possibility of using public/private keys (column 14 lines 7-43).

With regard to claims 5, 8, 9, 12, 13, and 16, Le discloses that the system has a default security level, and is being reconfigured (column 4 line 65-column 5 line 4).

With regard to claims 2, 18, 22, and 26, Le discloses a non-volatile memory, as outlined above, but does not specify a FLASH memory. The examiner takes official notice that flash memory is a commonly used type of non-volatile memory. It would have been obvious for one of ordinary skill in the art to use Le's cryptographic control in a system using FLASH memory to allow for dynamic capability control.

With regard to claims 6, 7, 10, 11, 14, and 15, Le discloses the method for initializing cryptographic functionality, as outlined above, but does not go into details of what to do when initialization fails. The examiner takes official notice that not allowing functionality or bringing down the system are common results to failed authentication attempts. It would have been obvious to one of ordinary skill in the art to use these default, in order to prevent unauthorized initializations.

Response to Arguments

3. Applicant's arguments filed 23 May 2005 have been fully considered but they are not persuasive.

With regard to applicant's argument that the chip in Le does not decrypt the initialization information, the examiner points out that Le does not disclose the initialization information should be encrypted, but gives motivation to encrypt it.

Art Unit: 2134

Applicant seems to be pointing out a lacking in Le that has already been addressed by the examiner. If Le explicitly taught the entire claim without modification, the examiner would have rejected the claims under 35 U.S.C. 102(b). The examiner rejected the claims under 35 U.S.C. 103(a) specifically because there were differences between Le and the claims, but that these modifications would have been obvious to one of ordinary skill in the art for Le's stated motivation that encrypting initialization information increases security (column 7 lines 5-15).

Conclusion

4. All claims are drawn to the same invention claimed in the application prior to the entry of the submission under 37 CFR 1.114 and could have been finally rejected on the grounds and art of record in the next Office action if they had been entered in the application prior to entry under 37 CFR 1.114. Accordingly, **THIS ACTION IS MADE FINAL** even though it is a first action after the filing of a request for continued examination and the submission under 37 CFR 1.114. See MPEP § 706.07(b). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire **THREE MONTHS** from the mailing date of this action. In the event a first reply is filed within **TWO MONTHS** of the mailing date of this final action and the advisory action is not mailed until after the end of the **THREE-MONTH** shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of

Art Unit: 2134

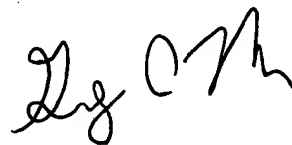
the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jacob Lipman whose telephone number is 571-272-3837. The examiner can normally be reached on M-Th 7 AM-3 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Gregory Morse can be reached on 571-272-3838. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

JL



GREGORY MORSE
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2100